

## 6<sup>th</sup> Prague Embedded Systems Workshop 2018

# PESW 2018

June 28–30, 2018, Roztoky u Prahy, Czech Republic  
**A forum for friendly discussion of emerging ideas  
aimed at doctoral and prospective doctoral students**

By **CTU in Prague**  
in cooperation with



**Student Branch  
CTU in Prague**

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### WORKSHOP WEB PAGE

<http://pesw.fit.cvut.cz/2018>

### BACKGROUND

The Prague Embedded Systems Workshop is a research meeting intended for the presentation and discussion of students' results and progress in all aspects of embedded systems design, testing and applications. It is organized by members of the Department of Digital Design at Faculty of Information Technology (which is the youngest) of the Czech Technical University in Prague (which is the oldest technical university in Central Europe). The workshop aims to enhance collaboration between different universities not only inside EU. It will be based on oral presentations, mutual communication and discussions.

### SUBMISSION TYPES

- **Full papers** (up to 10 pages, single-column) describing original research.
- **Abstracts** (one page, single-column) of earlier published successfully presented papers (at conferences, journals, etc.).
- **Student posters** - abstracts of defended Bc. and MSc. Theses with subsequent poster presentation. Will not be included in the Proceedings.

### SCOPE

The workshop PESW 2018 addresses emerging issues, hot problems, new solution methods, and their hardware and software implementations in the fields of digital and mixed-signal system design. It is especially focused on dependable and low power design, and testing methods related to the SoC technology and modern embedded applications. The workshop topics include (but are not limited to):

- Programmable/re-configurable/adaptable architectures
- SoC and NoC design and testing
- Digital design optimization methods
- Architectures and hardware for security applications
- On-line and off-line error detection and correction
- Fault-tolerant control systems design methods
- Testability and reliability analysis
- Logic synthesis and optimization
- Diagnosis & testing of embedded systems
- Verification of embedded systems
- Mission-critical & real-time systems
- Heterogeneous Cyber-Physical Systems
- Robotic control
- Applications of (embedded) digital systems

### Special Session on Network Security:

- Network monitoring and measurements
- Network traffic processing and analysis, anomaly detection
- Security and privacy issues

### STUDENT POSTER SESSION

- Are you in the final year of the Bc or MSc study?
- Is your thesis related to **Embedded Systems**
- Will your thesis be defended before the workshop dates
- **Attend the unique workshop**, present your defended thesis and **WIN!**
- The most beneficial works will be rewarded by IEEE and the workshop sponsors

### Gain valuable benefits:

- Discuss your work in the international community with representatives of **academia and industry**
- Take the workshop **for free**
- Find new professional **contacts and friends**
- Find a **job**

### IMPORTANT DATES

Student posters submission deadline:	June 24, 2018
Student posters notification of acceptance:	June 26, 2018
Workshop:	June 28–30, 2018